SAMSUNG

Memory Evolution through Open Source Collaboration

JongGyu Park | VP

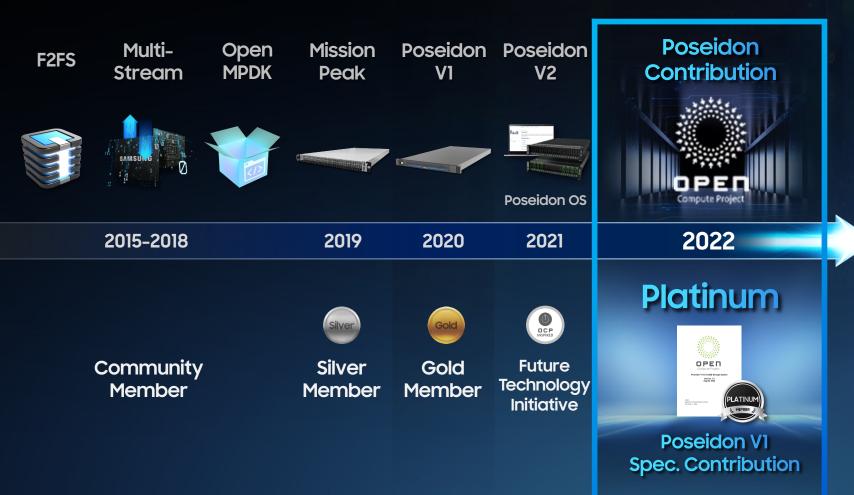
Head of Solution Product & Development Team Memory Business, Samsung Electronics



NOVEMBER 1, 2022 SEOUL, KOREA

Leading the Journey Into Open Source

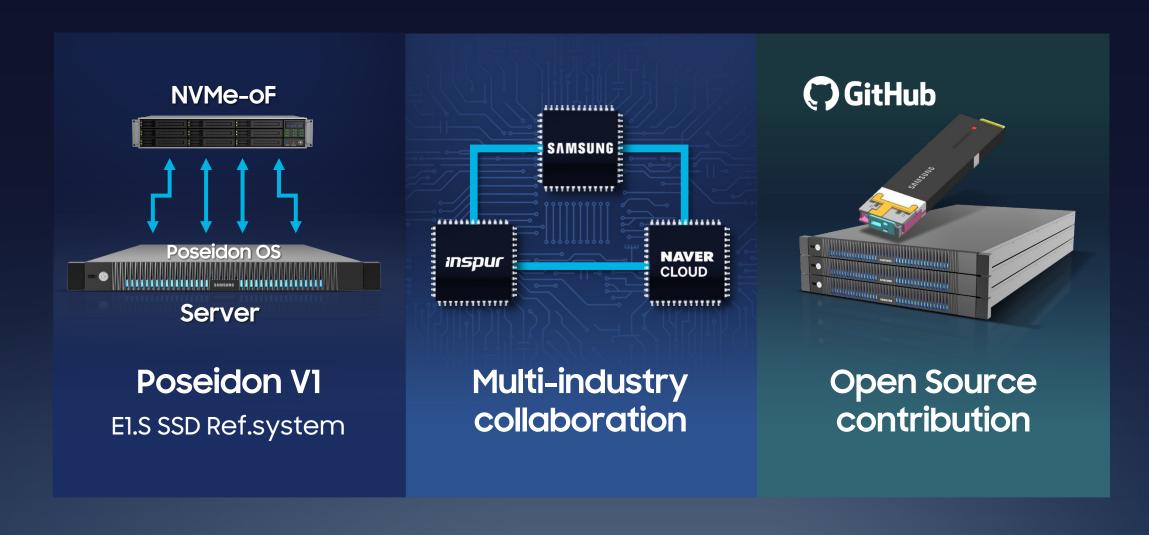
Poseidon Project



Support the full lineup of EDSFF form factors

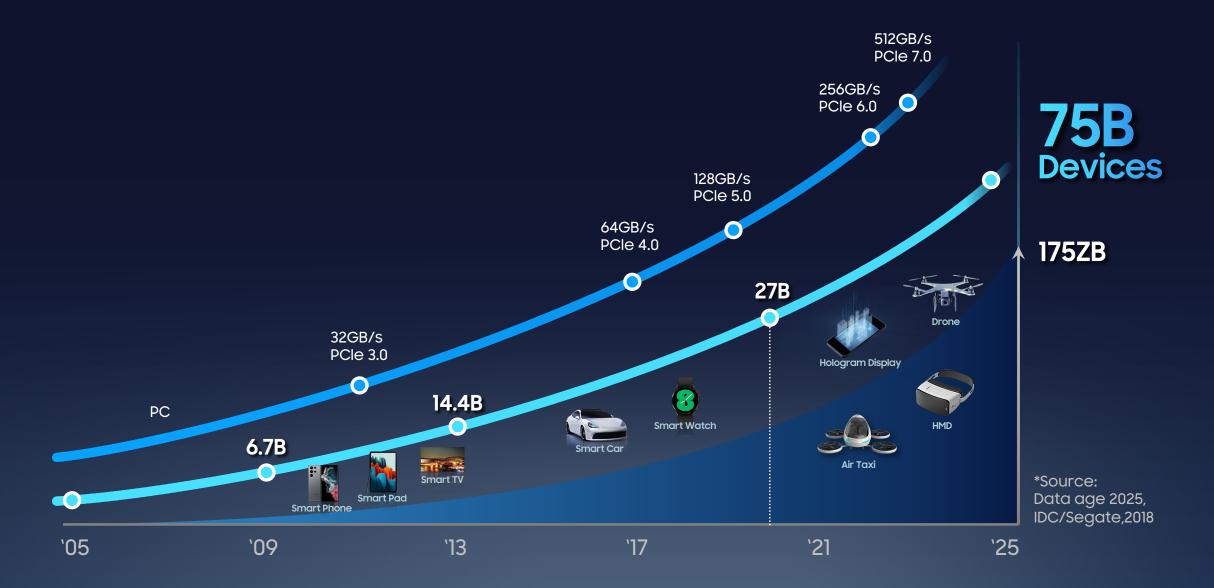
Poseidon Project

Leading the way toward innovative storage solutions built on open source



Samsung's Breakthrough Memory Solutions for Big Data

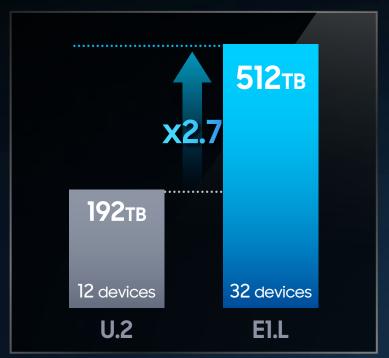
The Advent of the Data Age



New Standard Form Factor for Space & Power Efficiency

El optimized to 1U system

- Max 32 devices of 1U
- PM9A3 16TB x 32 (512TB)



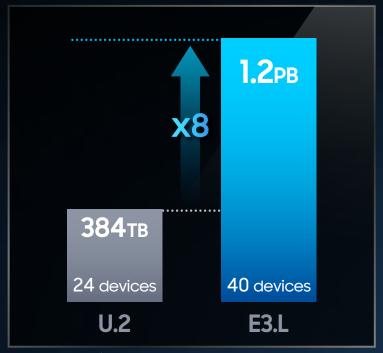


^{*} E1.S 32 devices

New Standard Form Factor for Space & Power Efficiency

E3 optimized to 2U system

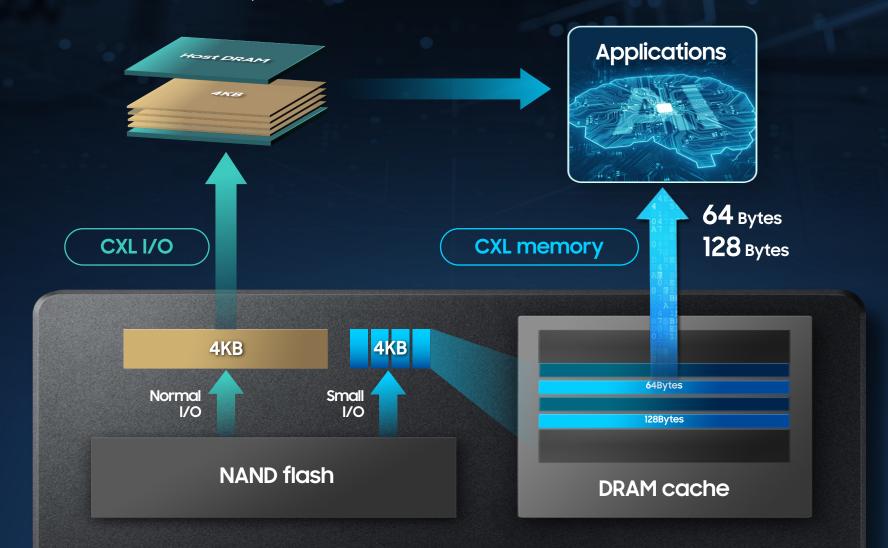
- Max 40 devices of 2U
- PM1743 32TB x 40 (1280TB)





Memory-Semantic SSD

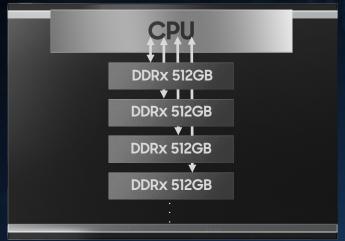
- Optimized for small-unit access to data stored in NAND to increase efficiency
- 20x better random read/write performance *compared to conventional SSDs



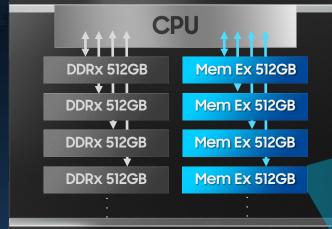
Memory Expander

- Enables a server to expand its memory capacity to tens of TBs
- 2x larger memory capacity *may vary depending on system configuration

Max 8TB for 1 CPU



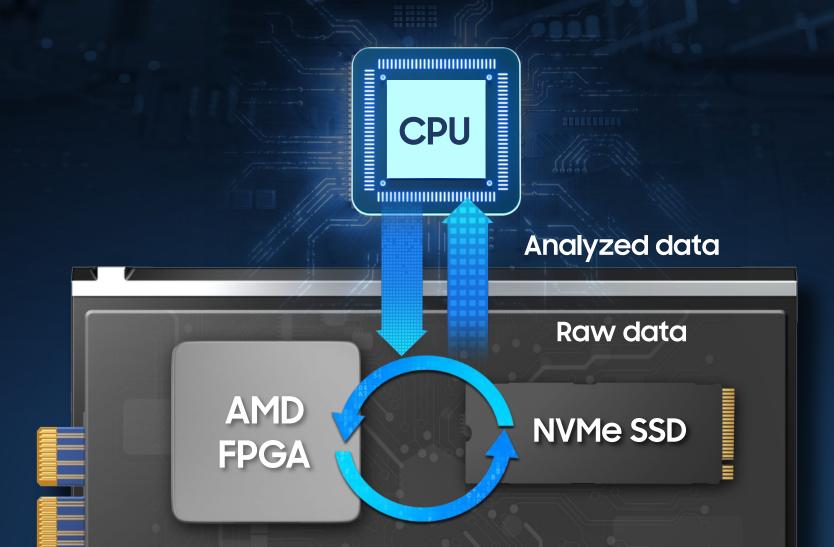
Max 16TB for 1 CPU





2nd Gen. SmartSSD

- Saves energy with novel near-data-processing hardware and software
- Cuts energy consumption by up to 70%, CPU utilization by up to 50% *compared to conventional SSDs



Storage sub-system Solutions

Pushing EDSFF E3 to the MAX Poseidon V2

PCIe 5.0-based open HW technology Higher performance

EDSFF E3 reference More capacity

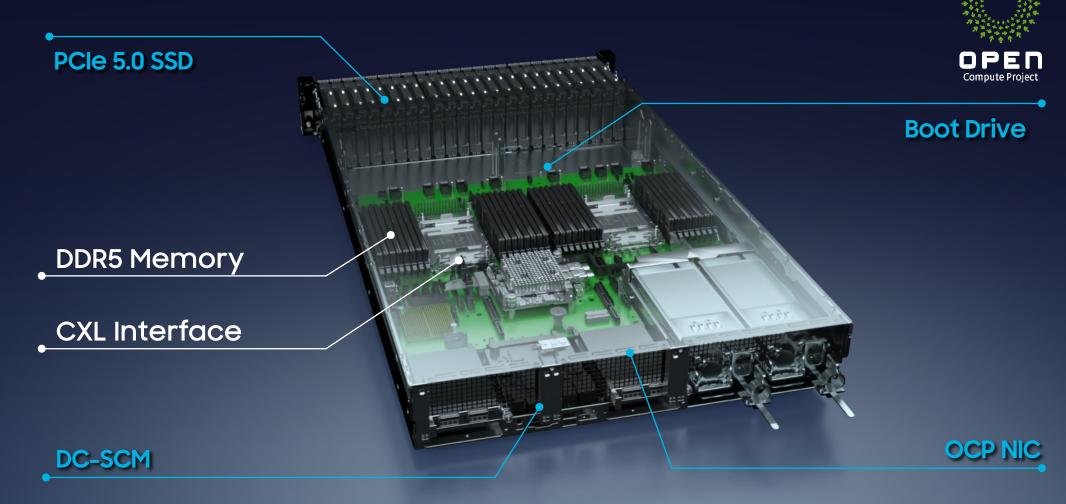
Two types of servers



Poseidon V2

Detailed components of Poseidon V2

Packed with innovative technologies and dynamic components



Composable Architecture of Poseidon v2

High-performance Shared storage E3.S SSD X 24

High memory server For cloud service

Memory Expander + SSD



Accelerator server For AI/ML

Smart SSD / Memory-semantic SSD X 8 High density server For archiving

128TB ZNS QLC

Petabyte SSD (PBSSD)

- Disaggregated NVMe subsystem with QLC SSDs increases rack-scale space and power efficiency
- Building block for a super high-density storage system











1PB disaggregated storage Box

Space efficiency | Power efficiency | High performance

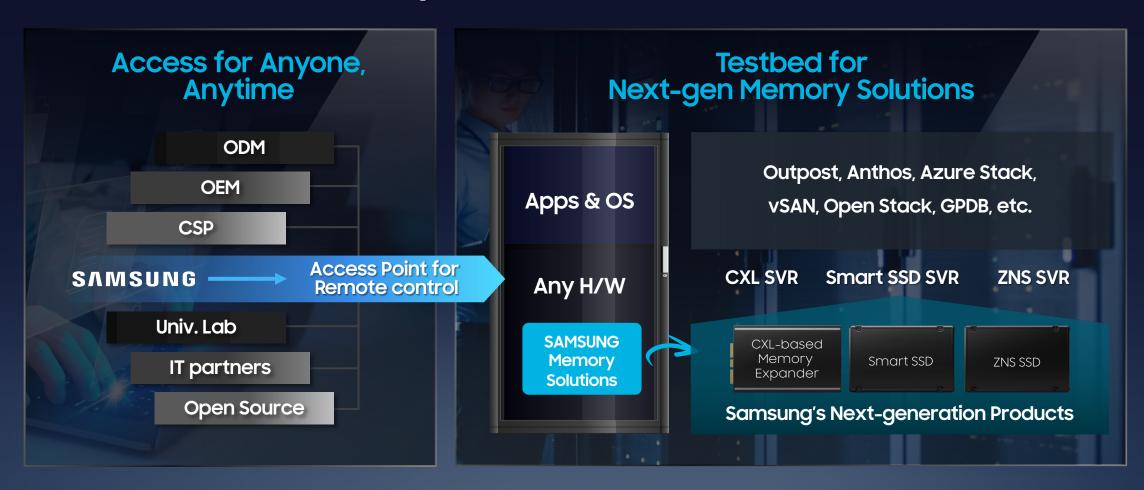


Exabyte scale storage system

Collaboration is the Future of Memory Solutions

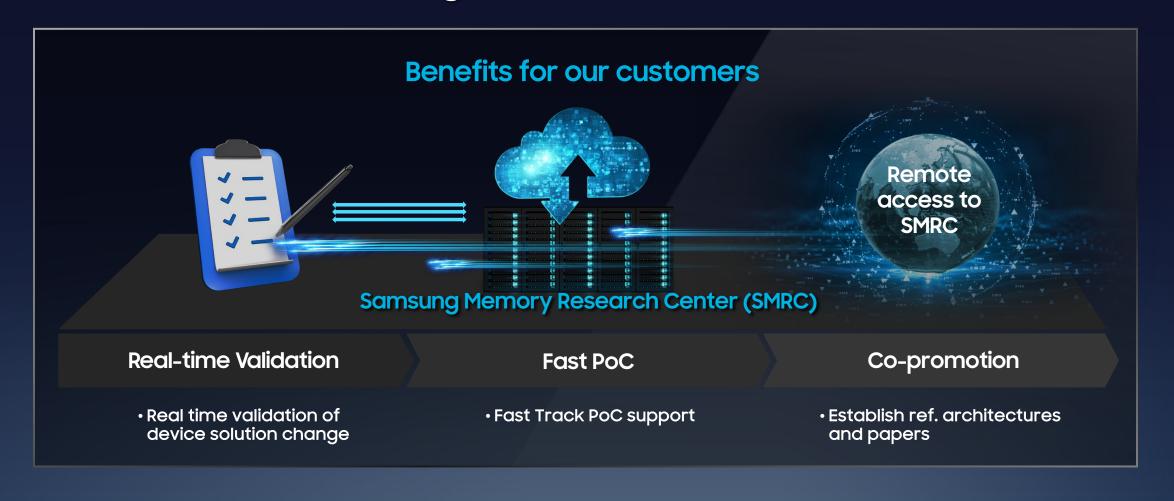
Collaboration is the Future of Memory Solutions

Key features of SMRC



Collaboration is the Future of Memory Solutions

Connecting the blocks for the future



Making Future Data Centers More Sustainable through the Manufacturing Process

Manufacturing: Less Carbon, More Reusable Energy

Using RCS (Regenerative Catalytic System) to reduce process gas, air pollutants

Process Gas



Electricity



Using renewable energy



High-efficiency facilities



**CDP

Waste Management: Zero Waste by 2025

Obtaining Platinum validation for Zero Waste to Landfill for all global sites

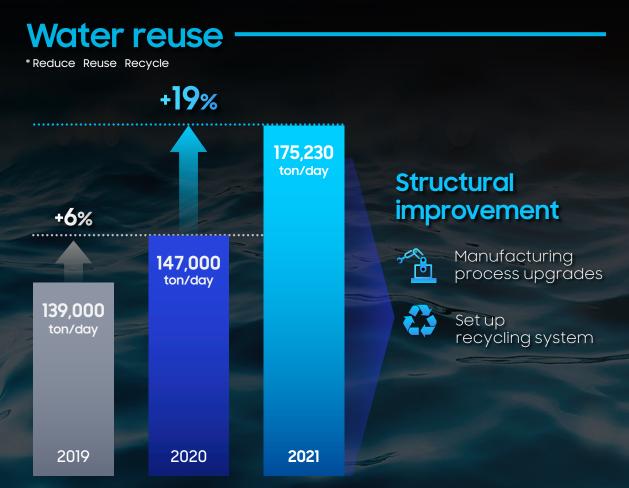
By 2025, we will obtain Platinum validation at each of our global semiconductor sites

We have achieved a 100% rate for recycled waste on all domestic sites



Water Management: Reduce, Reuse, Recycle

Improving water management and setting purification process



Advanced purification system

• Water used in manufacturing are classified into six categories and purified with matching techniques for each type



* at domestic manufacturing sites

Confidential 10 / 12



Certified sustainable efforts across the entire process of manufacturing

Awarded the Triple Standard for carbon, water and waste by the Carbon Trust



Our Promise for a Sustainable Future

Technology that makes technology sustainable













Net zero

by **2050**

Keep water withdrawal to 2021 levels in 2030

2021 water withdrawal = 2030 water withdrawal

99.9% Waste Recycling by 2030 Air/water pollutants in natural state by 2040

*Air = natural atmosphere
Water auglity = upper stream level

SEPI: New Environmental Management Guidelines

 Developed the Semiconductor Environmental Performance Index (SEPI) to provide guideline for implementing stricter sustainability policies

SEPI: Evaluation system



Semiconductor's contribution to green efforts

40%



Environmental management in the supply chain

20%



Green manufacturing efforts at production sites

40%



User environment benefits

+5%

Next Steps

Overcome the limitations of existing ESG evaluations

Help more companies implement stricter environmental policies in a principled manner

Appeal to the eco-friendly efforts of the semiconductor industry

Criteria for customer choice Standard for investor evaluations

Reducing Emissions through Industry Cooperation

 Working with various stakeholders to ensure our product are being designed, manufactured, supplied and used in an environmentally conscious manner

SEMI Sustainability Summit

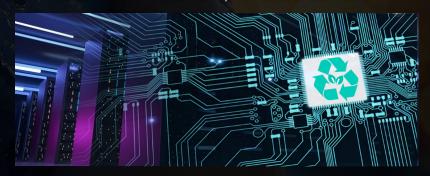
- First run in San Francisco in July 2022
- Share the methods and cases to reduce GHG emissions with the companies and suppliers in semiconductor industry
- Plan to participate the industry-wide efforts to reduce greenhouse gas emissions



with **Supply chain**

OCP Sustainability Initiative

- Initiative design system and parts that can be reused for users
- Share standards for LCA assessment of semiconductor usage stage
- Develop design and sourcing methods for circular economy of Data center



with **End-user**

SAMSUNG

